

<b>Notice of References Cited</b>		Application/Control No.	Applicant(s)/Patent Under Reexamination LEE ET AL.	
		Examiner Linda Wong	Art Unit 2611	Page 1 of 1

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*	B	US-6,690,243	02-2004	Henrion, Widge S.	331/117R
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*	V	Savoj et al, "A 10 Gb/s CMOS Clock and Data Recovery Circuit with Frequency Division", February 2001, 2001 IEEE International Solid-State Circuits Conference, pp 78-79, 434
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

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